

Form C: Type Test Verification Report

All Micro-generators connected to the **DNO Distribution Network** shall be **Fully Type Tested**. This form is the **Manufacturer's** declaration of compliance with the requirements of EREC G98.

This form should be used when making a Type Test submission to the Energy Networks Association (ENA) Type Test Register.

If the **Micro-generator** is **Fully Type Tested** and already registered with the ENA Type Test Register, the **Installation Document** should include the **Manufacturer's** Reference Number (the system reference), and this form does not need to be submitted.

Manufacturer's reference number		Clearline Inverter CSI3600	
Micro-generator technology		DNS G3 Grid-Connected PV Inverter (Inverter Models: CSI3600)	
Manufacturer name		Viridian Solar Limited	
Address		68 Stirling Way, Papworth, Cambridge CB23 3GY, UK	
Tel	+44 (0)1480 839 865	Fax	N/A
E-mail	info@viridiansolar.co.uk	Web site	www.viridiansolar.co.uk
Registered Capacity , use separate sheet if more than one connection option.	Connection Option		
	3.6	kW single phase, single, split or three phase system	
	/	kW three phase	
	/	kW two phases in three phase system	
	/	kW two phases split phase system	
Energy storage capacity for Electricity Storage devices		Not energy storage inverter	
Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above Fully Type Tested reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98.			
Signed	 KT Tan 26 th February 2024	On behalf of	 Viridian Solar Limited
<p>Note that testing can be done by the Manufacturer of an individual component or by an external test house.</p> <p>Where parts of the testing are carried out by persons or organisations other than the Manufacturer then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.</p>			

1. Operating Range: This test should be carried out as specified in A.1.2.10.

Pass or failure of the test should be indicated in the fields below (right hand side), for example with the statement "Pass", "No disconnection occurs", etc. Graphical evidence is preferred.

Test 1

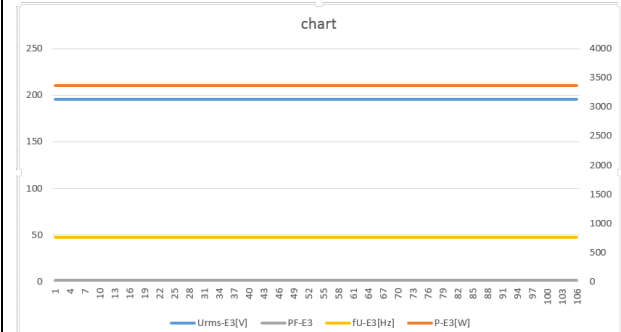
Voltage = 85% of nominal (195.5 V)

Frequency = 47.0 Hz

Power factor = 1

Period of test 20 seconds

Result: Pass



Test 2

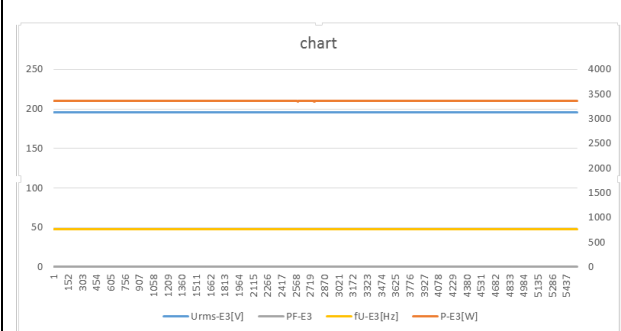
Voltage = 85% of nominal (195.5 V)

Frequency = 47.5 Hz

Power factor = 1

Period of test 90 minutes

Result: Pass



Test 3

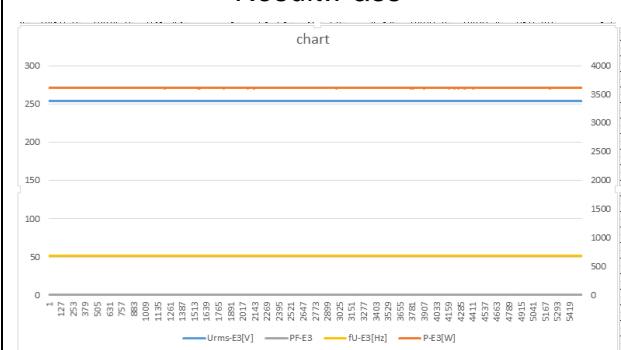
Voltage = 110% of nominal (253 V).

Frequency = 51.5 Hz

Power factor = 1

Period of test 90 minutes

Result: Pass



Test 4

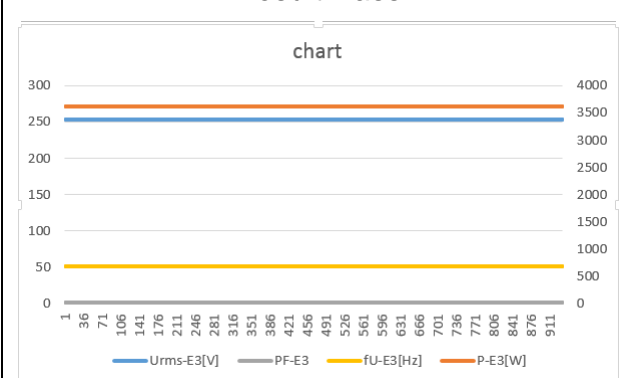
Voltage = 110% of nominal (253 V).

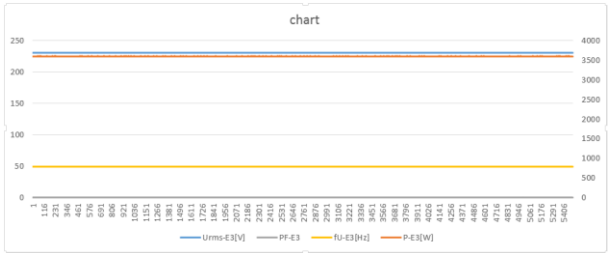
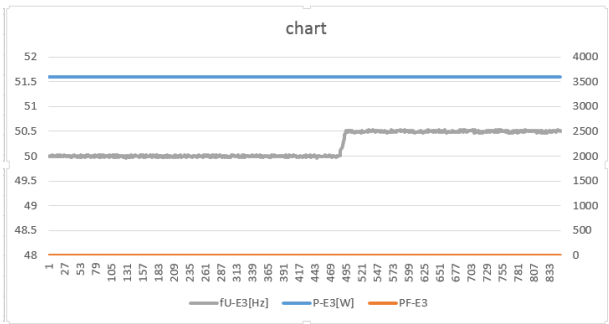
Frequency = 52.0 Hz

Power factor = 1

Period of test 15 minutes

Result: Pass



<p>Test 5</p> <p>Voltage = 100% of nominal (230 V).</p> <p>Frequency = 50.0 Hz</p> <p>Power factor = 1</p> <p>Period of test 90 minutes</p>	<p>Result: Pass</p> 
<p>Test 6 RoCoF withstand</p> <p>Confirm that the Micro-Generating Plant is capable of staying connected to the Distribution Network and operate at rates of change of frequency up to 1 Hzs^{-1} as measured over a period of 500 ms.</p>	<p>Result: Pass</p> 

2.Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2. The chosen test should be undertaken with a fixed source of energy at two power levels a) between 45 and 55% and b) at 100% of **Registered Capacity**. The test requirements are specified in Annex A1 A.1.3.1 (**Inverter** connected) or Annex A2 A.2.3.1 (Synchronous).

Micro-generator tested to BS EN 61000-3-2

Micro-generator rating per phase
(rpp)

3.6

kW

For 3-phase **Micro-generators**, tick this box if harmonic measurements are identical for all three phases. If the harmonics are not identical for each phase, please replicate this section with the results for each phase.

Harmo nic	At 45-55% of Registered Capacity ¹		100% of Registered Capacity			
	Measured Value MV in Amps		Measured Value MV in Amps		Limit in BS EN 61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2	0.022		0.030		1.080	
3	0.019		0.029		2.300	
4	0.008		0.012		0.430	
5	0.011		0.007		1.140	
6	0.004		0.008		0.300	
7	0.019		0.020		0.770	
8	0.005		0.008		0.230	
9	0.028		0.033		0.400	
10	0.004		0.007		0.184	
11	0.083		0.109		0.330	
12	0.008		0.007		0.153	
13	0.064		0.098		0.210	
14	0.006		0.006		0.131	
15	0.048		0.084		0.150	
16	0.004		0.005		0.115	

¹ See the note in A.2.3.1 if 45-55% of **Registered Capacity** is below the minimum stable operating level. If an alternative loading level is chosen, the level should be indicated on the test form and the reason for not testing at 45-55% of **Registered Capacity** should be stated. The additional comments box at the end of the harmonics test sheet can be used for this.

17	0.034		0.070		0.132	
18	0.005		0.005		0.102	
19	0.023		0.059		0.118	
20	0.005		0.006		0.092	
21	0.015		0.048		0.107	0.160
22	0.005		0.006		0.084	
23	0.009		0.039		0.098	0.147
24	0.004		0.005		0.077	
25	0.006		0.032		0.090	0.135
26	0.004		0.005		0.071	
27	0.004		0.026		0.083	0.124
28	0.004		0.005		0.066	
29	0.004		0.022		0.078	0.117
30	0.003		0.005		0.061	
31	0.004		0.017		0.073	0.109
32	0.004		0.005		0.058	
33	0.004		0.016		0.068	0.102
34	0.004		0.005		0.054	
35	0.005		0.013		0.064	0.096
36	0.005		0.005		0.051	
37	0.006		0.012		0.061	0.091
38	0.010		0.009		0.048	
39	0.010		0.014		0.058	0.087
40	0.025		0.025		0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Additional comments:

3.Power Quality – Voltage fluctuations and Flicker: These tests should be undertaken in accordance with EREC G98 Annex A1 A.1.3.3 (**Inverter** connected) or Annex A2 A.2.3.3 (Synchronous).

The standard test impedance is 0.4 Ω for a single phase **Micro-generating Plant** (and for a two phase unit in a three phase system) and 0.24 Ω for a three phase **Micro-generating Plant** (and for a two phase unit in a split phase system). Please ensure that both test and standard impedance are completed on this form. If the test impedance (or the measured impedance) is different to the standard impedance, it must be normalised to the standard impedance as follows (where the **Power Factor** of the generation output is 0.98 or above):

$d \text{ max normalised value} = (\text{Standard impedance} / \text{Measured impedance}) \times \text{Measured value}.$

Where the **Power Factor** of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the standard impedance.

The stopping test should be a trip from full load operation.

The duration of these tests needs to comply with the particular requirements set out in the testing notes for the technology under test.

The test date and location must be declared.

Test start date	2022.9.21			Test end date	2022.9.21			
Test location	Test lab of GoodWe Technologies Co., Ltd (No.90 Zijin Rd., New District, Suzhou, 215011, China)							
	Starting			Stopping			Running	
	d(max)	d(c)	d(t)	d(max)	d(c)	d(t)	P _{st}	P _{lt} 2 hours
Measured Values at test impedance	0.132 %	0.10 8%	0.0%	0.358%	0.318%	0.0%	0.057	0.044
Normalised to standard impedance	0.132 %	0.10 8%	0.0%	0.358%	0.318%	0.0%	0.057	0.044
Normalised to required maximum impedance	NA	NA	NA	NA	NA	NA	NA	NA
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65

Test Impedance	R	0.4	Ω	X	0.25	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	X	0.15 * 0.25 ^	Ω
Maximum Impedance	R	NA	Ω	X	NA	Ω

*Applies to three phase and split single phase **Micro-generators**. Delete as appropriate.

^ Applies to single phase **Micro-generators** and **Micro-generators** using two phases on a three phase system. Delete as appropriate.

4.Power quality – DC injection: This test should be carried out in accordance with A 1.3.4 as applicable.

The % **DC** injection (“as % of rated AC current” below) is calculated as follows:

% **DC** injection = Recorded **DC** value in Amps / base current

where the base current is the **Registered Capacity** (W) / 230 V. The % **DC** injection should not be greater than 0.25%.

Test power level	20%	50%	75%	100%
Recorded DC value in Amps	0.0143A	0.0144A	0.0112A	0.0124A
as % of rated AC current	0.091%	0.092%	0.072%	0.079%
Limit	0.25%	0.25%	0.25%	0.25%

5.Power Quality – Power factor: This test shall be carried out in accordance with A.1.3.2 and A.2.3.2 at three voltage levels and at **Registered Capacity** and the measured **Power Factor** must be greater than 0.95 to pass. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test.

	216.2 V	230 V	253 V
Measured value	0.9977	0.9978	0.9978
Power Factor Limit	>0.95	>0.95	>0.95

6.Protection – Frequency tests: These tests should be carried out in accordance with Annex A1 A.1.2.3 (**Inverter** connected) or Annex A2 A.2.2.3 (Synchronous). For trip tests, frequency and time delay should be stated. For “no trip tests”, “no trip” can be stated.

Function	Setting	Trip test	“No trip tests”
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	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5 Hz	20 s	47.48 Hz	20.05s	47.7 Hz 30 s	no trip
U/F stage 2	47 Hz	0.5 s	46.98 Hz	0.52s	47.2 Hz 19.5 s	no trip
					46.8 Hz 0.45 s	no trip
O/F stage 1	52 Hz	0.5 s	52.02 Hz	0.52s	51.8 Hz 120.0 s	no trip
					52.2 Hz 0.45 s	no trip

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the protection can be used. The “No trip tests” need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A1 A.1.2.2 (Inverter connected) or Annex A2 A.2.2.2 (Synchronous). For trip tests, voltage and time delay should be stated. For “no trip tests”, “no trip” can be stated.

Function	Setting		Trip test		“No trip tests”	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V	184 V	2.5 s	183.55V	2.52s	188 V 5.0 s	no trip
					180 V 2.45 s	no trip
O/V stage 1	262.2 V	1.0 s	262.13V	1.02s	258.2 V 5.0 s	no trip
O/V stage 2	273.7 V	0.5 s	274.15	0.52s	269.7 V 0.95 s	no trip
					277.7 V 0.45 s	no trip

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8. Protection – Loss of Mains test: For PV Inverters shall be tested in accordance with BS EN 62116. Other Micro-generators should be tested in accordance with A.2.2.4 at 10%, 55% and 100% of rated power.

To be carried out at three output power levels with a tolerance of plus or minus 5% in Test Power levels.²

² See the note in A.2.2.4 if the suggested loading levels are below the minimum stable operating level. If alternative loading levels are chosen, the level should be indicated on the test form and the reason for not testing at 10%/55% of **Registered Capacity** should be stated. The additional comments box at the end of the loss of mains test sheet can be used for this.

Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Limit is 0.5 s	NA	NA	NA	NA	NA	NA
For Multi phase Micro-generators confirm that the device shuts down correctly after the removal of a single fuse as well as operation of all phases.						
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph1 fuse removed	NA	NA	NA	NA	NA	NA
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph2 fuse removed	NA	NA	NA	NA	NA	NA
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph3 fuse removed	NA	NA	NA	NA	NA	NA
Note for technologies which have a substantial shut down time this can be added to the 0.5 s in establishing that the trip occurred in less than 0.5 s. Maximum shut down time could therefore be up to 1.0 s for these technologies.						
Indicate additional shut down time included in above results.				NA ms		
Additional comments:						

For **Inverters** tested to BS EN 62116 the following sub set of tests should be recorded in the following table.

Test Power and imbalance	33%	66%	100%	33%	66%	100%
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	-5% Q Test 22	-5% Q Test 12	-5% P Test 5	+5% Q Test 31	+5% Q Test 21	+5% P Test 10
Trip time. Limit is 0.5 s ³	0.470s	0.400s	0.420s	0.470s	0.490s	0.410s
9.Protection – Frequency change, Vector Shift Stability test: This test should be carried out in accordance with EREC G98 Annex A1 A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous). Confirmation is required that the Micro-generating Plant does not trip under positive / negative vector shift.						
	Start Frequency	Change	Confirm no trip			
Positive Vector Shift	49.0 Hz	+50 degrees	no trip			
Negative Vector Shift	50.0 Hz	- 50 degrees	no trip			
10.Protection – Frequency change, RoCoF Stability test: The requirement is specified in section 11.3, test procedure in Annex A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous). Confirmation is required that the Micro-generating Plant does not trip for the duration of the ramp up and ramp down test.						
Ramp range	Test frequency ramp:	Test Duration	Confirm no trip			
49.0 Hz to 51.0 Hz	+0.95 Hzs ⁻¹	2.1 s	no trip			
51.0 Hz to 49.0 Hz	-0.95 Hzs ⁻¹	2.1 s	no trip			
11.Limited Frequency Sensitive Mode – Over frequency test: This test should be carried out in accordance with A.1.2.8. The test should be carried out using the specific threshold frequency of 50.4 Hz and Droop of 10%. The measurement tolerances are contained in A.1.2.8.						
Test sequence at Registered Capacity >80%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient Droop (%)		
Step a) 50.00 Hz ±0.01 Hz	3598	50	3681	/		
Step b) 50.45 Hz ±0.05 Hz	3559	50.45		9.23%		
Step c) 50.70 Hz ±0.10 Hz	3361	50.7		9.11%		
Step d) 51.15 Hz ±0.05 Hz	3007	51.15		9.14%		
Step e) 50.70 Hz ±0.10 Hz	3357	50.7		8.96%		
Step f) 50.45 Hz ±0.05 Hz	3568	50.45		12.00%		
Step g) 50.00 Hz ±0.01 Hz	3604	50		/		

³ If the device requires additional shut down time (beyond 0.5s but less than 1s) then this should be stated on this form.

Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient Droop(%)
Step a) 50.00 Hz ± 0.01 Hz	1805	50	1835	/
Step b) 50.45 Hz ± 0.05 Hz	1766	50.45		9.23%
Step c) 50.70 Hz ± 0.10 Hz	1568	50.7		9.11%
Step d) 51.15 Hz ± 0.05 Hz	1212	51.15		9.11%
Step e) 50.70 Hz ± 0.10 Hz	1568	50.7		9.11%
Step f) 50.45 Hz ± 0.05 Hz	1767	50.45		9.47%
Step g) 50.00 Hz ± 0.01 Hz	1801	50		/

12. Power output with falling frequency test: This test should be carried out in accordance with A.1.2.7.

Test sequence	Measured Active Power Output	Frequency	Primary power source
Test a) 50 Hz ± 0.01 Hz	3601	50	3681
Test b) Point between 49.5 Hz and 49.6 Hz	3599	49.55	3680
Test c) Point between 47.5 Hz and 47.6 Hz	3598	47.55	3680

NOTE: The operating point in Test (b) and (c) shall be maintained for at least 5 minutes

13. Re-connection timer.

Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 2. Both the time delay setting and the measured delay should be provided in this form; both should be greater than 20 s to pass. Confirmation should be provided that the **Micro-generating Plant** does not reconnect at the voltage and frequency settings below; a statement of "no reconnection" can be made.

Time delay setting	Measured delay		Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 2.			
48s	48s		At 266.2 V	At 180.0 V	At 47.4 Hz	At 52.1 Hz
Confirmation that the Micro-generator does not re-connect.			No re-connection	no re-connection	no re-connection	no re-connection

14. Fault level contribution: These tests shall be carried out in accordance with EREC G98 Annex A1 A.1.3.5 (**Inverter** connected) and Annex A2 A.2.3.4 (Synchronous). Please complete each entry, even if the fault contribution is zero.

For machines with electro-magnetic output	For Inverter output
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Parameter	Symbol	Value	Time after fault	Volts	Amps
Peak Short Circuit current	i_p	NA	20 ms	33	17.29
Initial Value of aperiodic current	A	NA	100 ms	32.6	18.4
Initial symmetrical short-circuit current*	I_k	NA	250 ms	32.8	17.76
Decaying (aperiodic) component of short circuit current*	i_{DC}	NA	500 ms	33	17.45
Reactance/Resistance Ratio of source*	X/R	NA	Time to trip	2.75	In seconds
For rotating machines and linear piston machines the test should produce a 0 s – 2 s plot of the short circuit current as seen at the Micro-generator terminals.					
* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot					
Logic Interface (input port)					
Confirm that an input port is provided and can be used to reduce the Active Power output to zero					Yes
Provide high level description of logic interface, e.g. details in 9.4.3 such as AC or DC signal (the additional comments box below can be used)					Yes
Self-Monitoring solid state switching: No specified test requirements. Refer to EREC G98 Annex A1 A.1.3.6 (Inverter connected).					Yes
It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator , the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.					
Cyber security					
Confirm that the Manufacturer or Installer of the Micro-generator has provided a statement describing how the Micro-generator has been designed to comply with cyber security requirements, as detailed in 9.7.					Yes
Additional comments					
The following documents are attached to this declaration: - “Clearline Inverters DNS declaration about Logic Interface”, as for required in “Logic Interface” section. - “Clearline Inverters DNS declaration about cyber-security”, as for required in “Cyber security” section;					